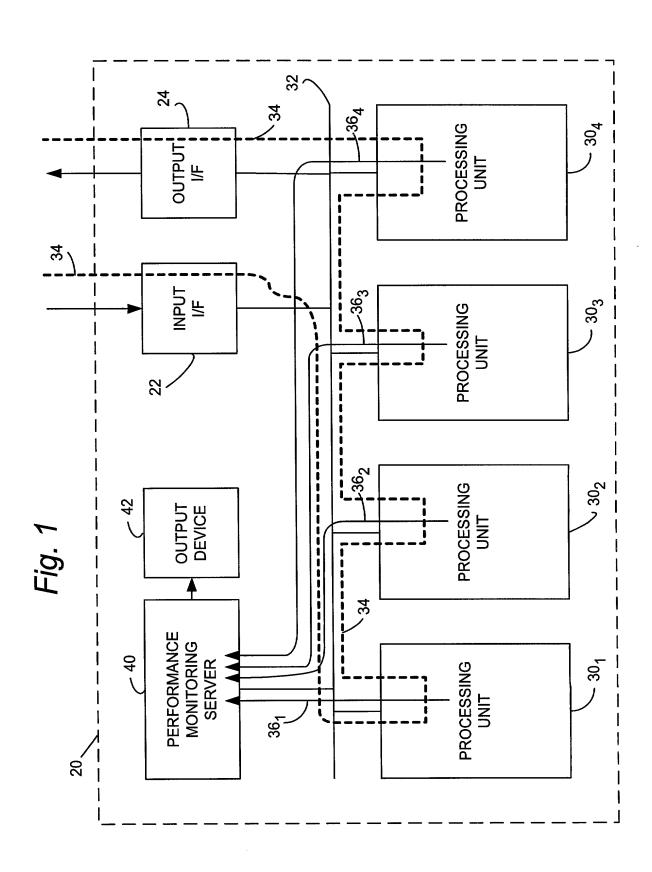
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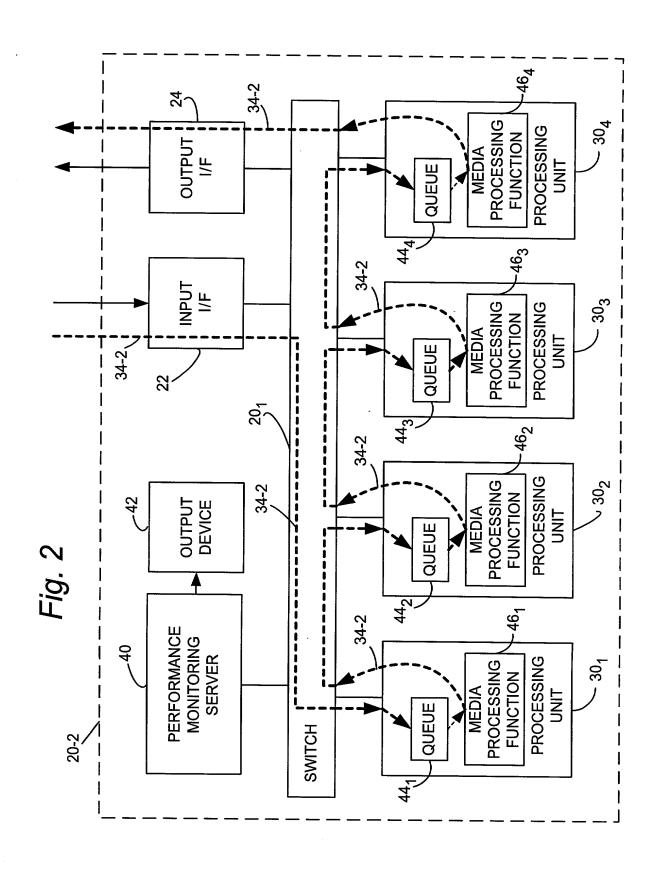


Fig. 3

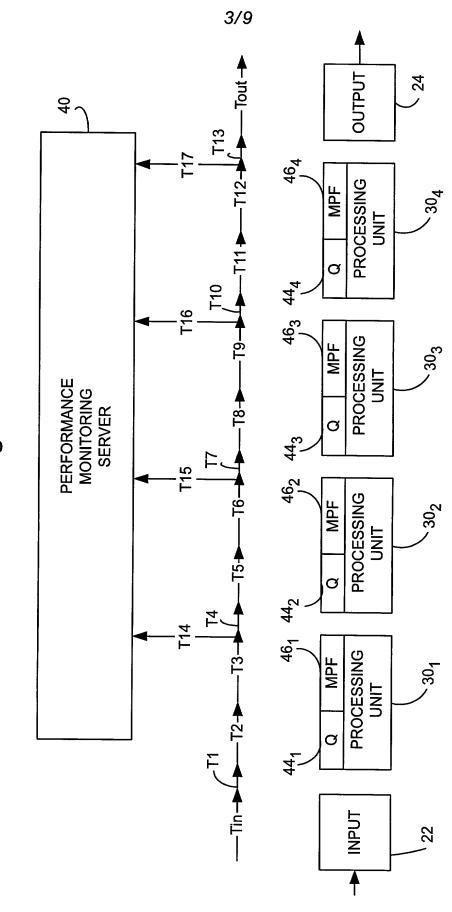
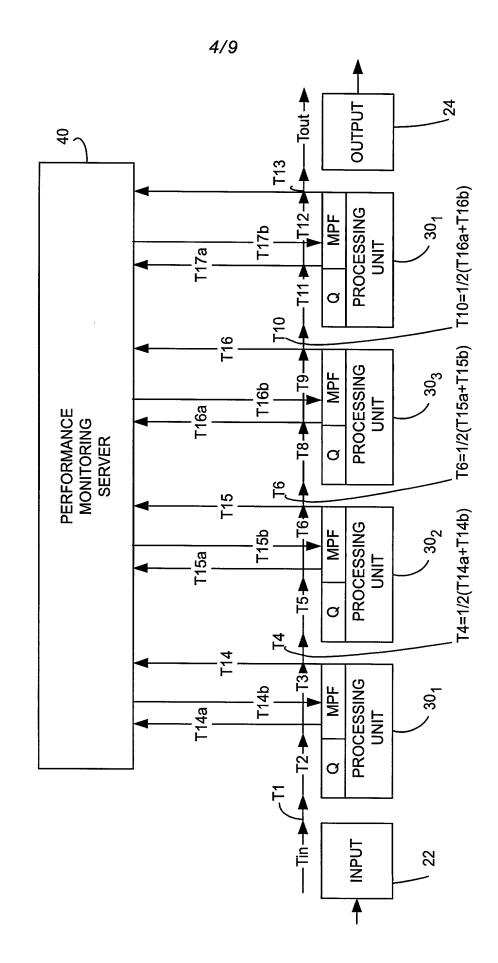
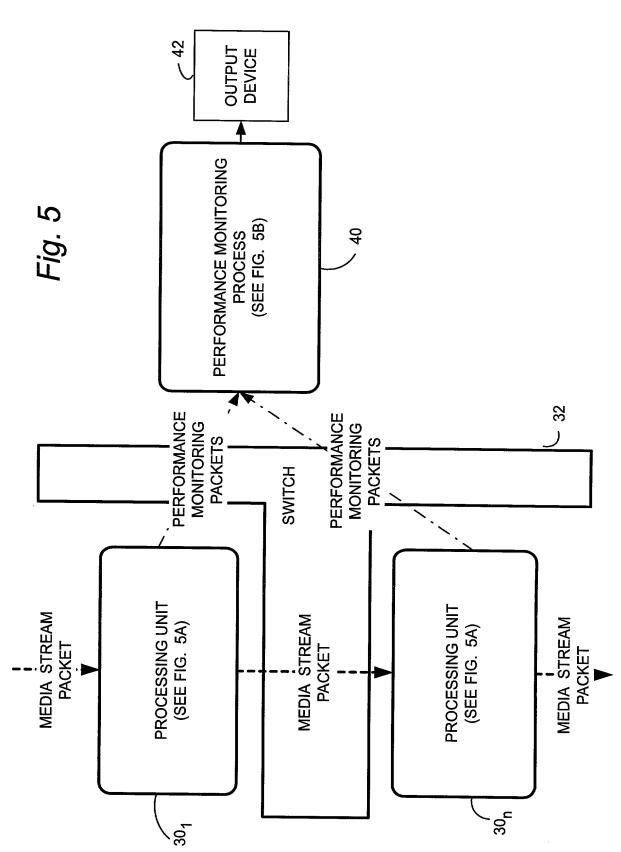
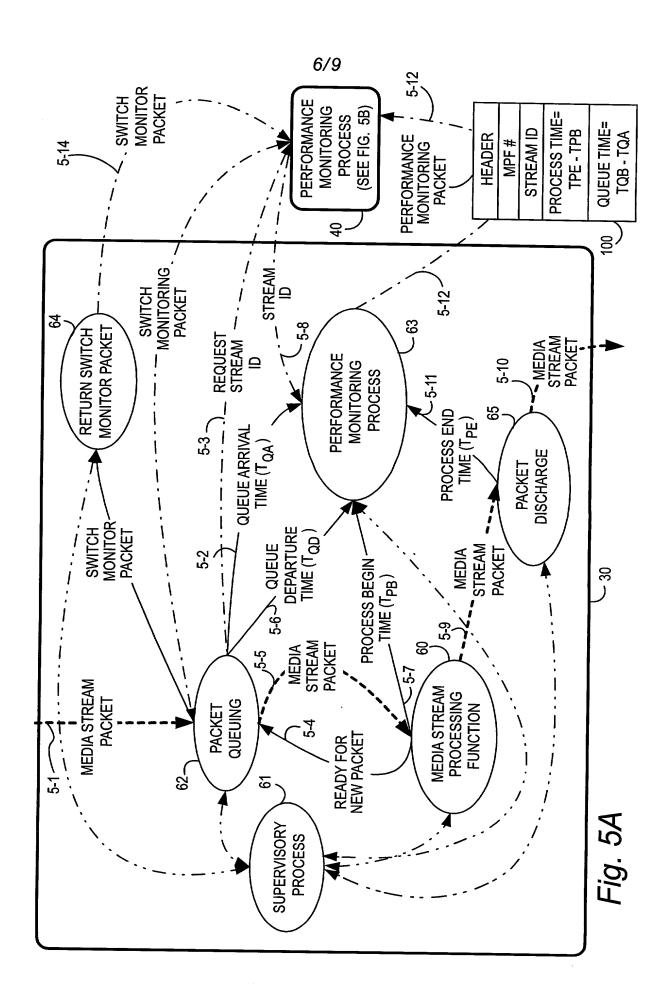


Fig. 4







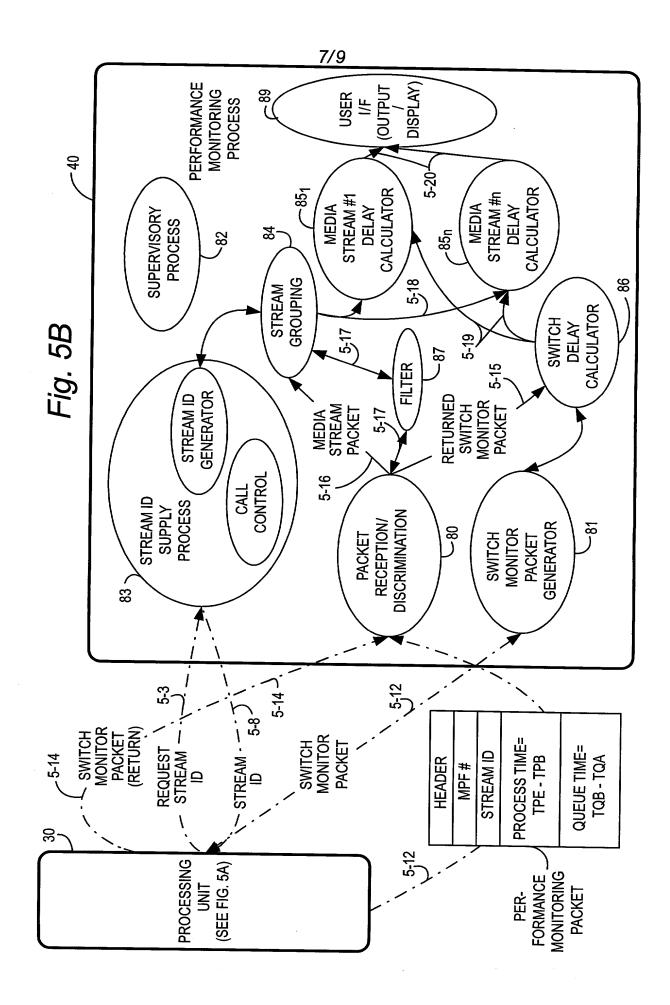


Fig. 6

	QD <sub>1-1</sub>	PD <sub>1-1</sub>	TD <sub>1-1</sub>	QD <sub>1-2</sub>	PD <sub>1-2</sub>	TD <sub>1-2</sub>	
MEDIA STREAM # 1	QUEUE DELAY	PROCESSING DELAY	TOTAL DELAY	QUEUE DELAY	PROCESSING DELAY	TOTAL DELAY	
MED	FIRST PROCESSOR (30 <sub>1</sub> )		SECOND PROCESSOR (30 <sub>2</sub> )				

FIRST PROCESSING (30 <sub>1</sub> ) SECOND SECOND PROCESSING DELAY TD <sub>0</sub> QUEUE DELAY TD <sub>0</sub> QUEUE DELAY TD <sub>0</sub> PROCESSING PD <sub>0</sub> PROCESSING PD <sub>0</sub>	MED	MEDIA STREAM # q	
PROCESSING DELAY TOTAL DELAY QUEUE DELAY PROCESSING DELAY	FIRST	QUEUE DELAY	QD <sub>q-1</sub>
AUEUE DELAY PROCESSING DELAY DELAY	PROCESSOR (30.)	PROCESSING DELAY	PD <sub>q-1</sub>
QUEUE DELAY PROCESSING DELAY	(kg)	TOTAL DELAY	TD <sub>q-1</sub>
PROCESSING DELAY	SECOND	QUEUE DELAY	QD <sub>q-2</sub>
	PROCESSOR (30.)	PROCESSING DELAY	PD <sub>q-2</sub>
TOTAL DELAY	(202)	TOTAL DELAY	TD <sub>q-2</sub>

			<b></b>
QD <sub>1-4</sub>	PD <sub>1-4</sub>	TD <sub>1-4</sub>	OND1
QUEUE DELAY	PROCESSING DELAY	TOTAL DELAY	ODE DELAY
FOURTH	OVERALL NODE DELAY		

OND,	
DELAY	
NODE	
OVERALL	

Fig. 7

